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## CERTIFICATE OF MAILING PURSUANT TO 37 C.F.R. 1.8

In re Application of: Jeff C. Klein, et al.

Application No.: 10/046,937

Filed: January 14, 2002

Title: Verification Test Method for Programmable Logic Devices

Docket: H0002065

### Type of Filing:

- 1) Disclosure Statement Under 37 C.F.R. §1.56
- 2) Information Disclosure Statement by Applicant  
Form 1449A/PTO (2 pgs.)
- 3) Copies of Disclosed References (11)
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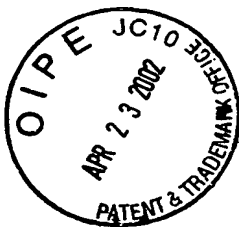
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeff C. Klein, et al.

Serial No.: 10/046,937

Filed: January 14, 2002

For: VERIFICATION TEST METHOD FOR  
PROGRAMMABLE LOGIC DEVICES

Group Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

**INFORMATION DISCLOSURE STATEMENT**Assistant Commissioner for Patents  
Washington, DC 20231

Sir:

Applicant hereby brings to the attention of the Examiner the documents noted on the accompanying Form PTO-1449. This information disclosure statement is being filed before receipt of a first Office Action; therefore, under 37 C.F.R. § 1.97(b)(3), no fee is believed due.

It is respectfully requested that the information cited herein be expressly considered during the prosecution of this application and made of record on any patent to issue therefrom.

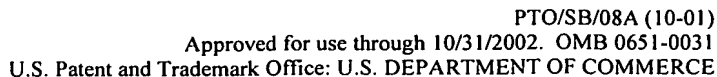
Respectfully submitted,

Dated: 4/17/02

By:

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Substitute for form 1449A/PTO

*(use as many sheets as necessary)*

Sheet	1	of	2
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**Complete if Known**

<b>Application Number</b>	10/046,937
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<b>Filing Date</b>	January 14, 2002
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First Named Inventor	Jeff C. Klein
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## Group Art Artist

Examiner Name

Attorney Docket Number	H0002065
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## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

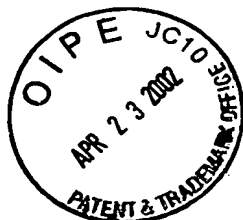
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Signature**

Date  
Considered

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<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3.). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark if English language Translation is attached.

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PTO/SB/08B (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Application Number</b>	10/046,937
		<b>Filing Date</b>	January 14, 2002
		<b>First Named Inventor</b>	Jeff C. Klein
		<b>Group Art Artist</b>	
		<b>Examiner Name</b>	
<b>Sheet</b>	2 of 2	<b>Attorney Docket Number</b>	H0002065

**OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Implementation Technology, Chapter 3, April 5, 1999, Pages 61-100.	
		XESS CORPORATION, "What are CPLDs and FPGAs?", <a href="http://www.xess.com/fpgatut.htm">http://www.xess.com/fpgatut.htm</a> , Date visited: 3/20/02, 3 pages.	
		RICHARD GEISSLER, "1.2.1 Basics of Synthesis and Optimization", <a href="http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node10.html">http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node10.html</a> , Date visited: 3/20/02, 2 pages.	
		RICHARD GEISSLER, "1.2.2 Logic-Level Optimization", <a href="http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node11.html">http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node11.html</a> , Date visited: 3/20/02, 3 pages.	
		RICHARD GEISSLER, "1.3 Synthesis Design Flow", <a href="http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node12.html">http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node12.html</a> , Date visited: 3/20/02, 3 pages.	
		Language Overview, "What is VHDL", <a href="http://www.acc-eda.com/vhdlref/refguide/language_overview/language_overview.htm">www.acc-eda.com/vhdlref/refguide/language_overview/language_overview.htm</a> , Date visited: 12/26/01, 3 pages.	
		Language Overview, "Test Benches", <a href="http://www.acc-eda.com/vhdlref/refguide/language_overview/test_benches/test_benches.htm">www.acc-eda.com/vhdlref/refguide/language_overview/test_benches/test_benches.htm</a> , Date visited: 12/26/01, 11 pages.	

Examiner Signature		Date Considered	
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<sup>1</sup>Unique citation designation number. <sup>2</sup>Applicant is to place a check mark if English language Translation is attached.

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